



FIG. 7

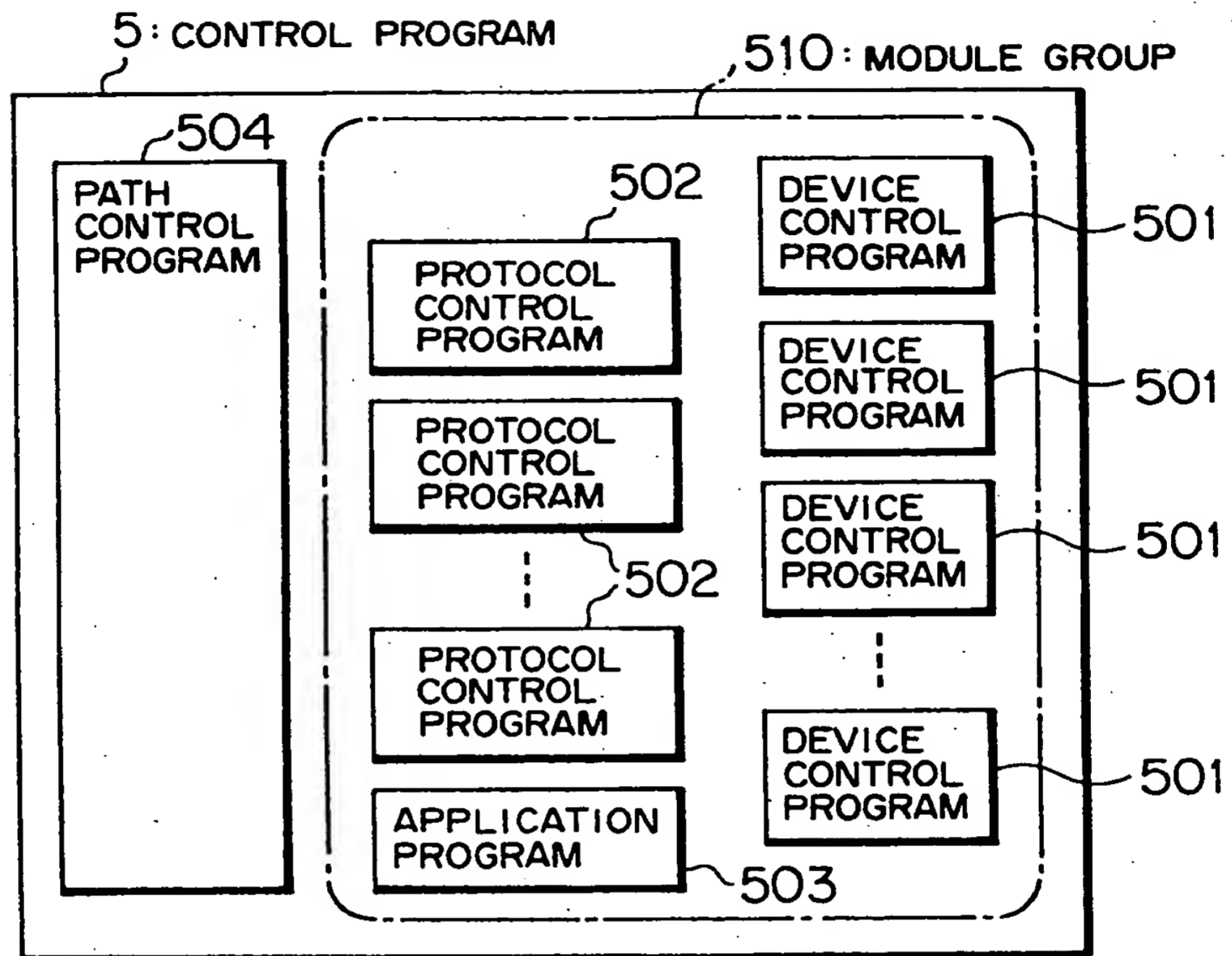


FIG. 8

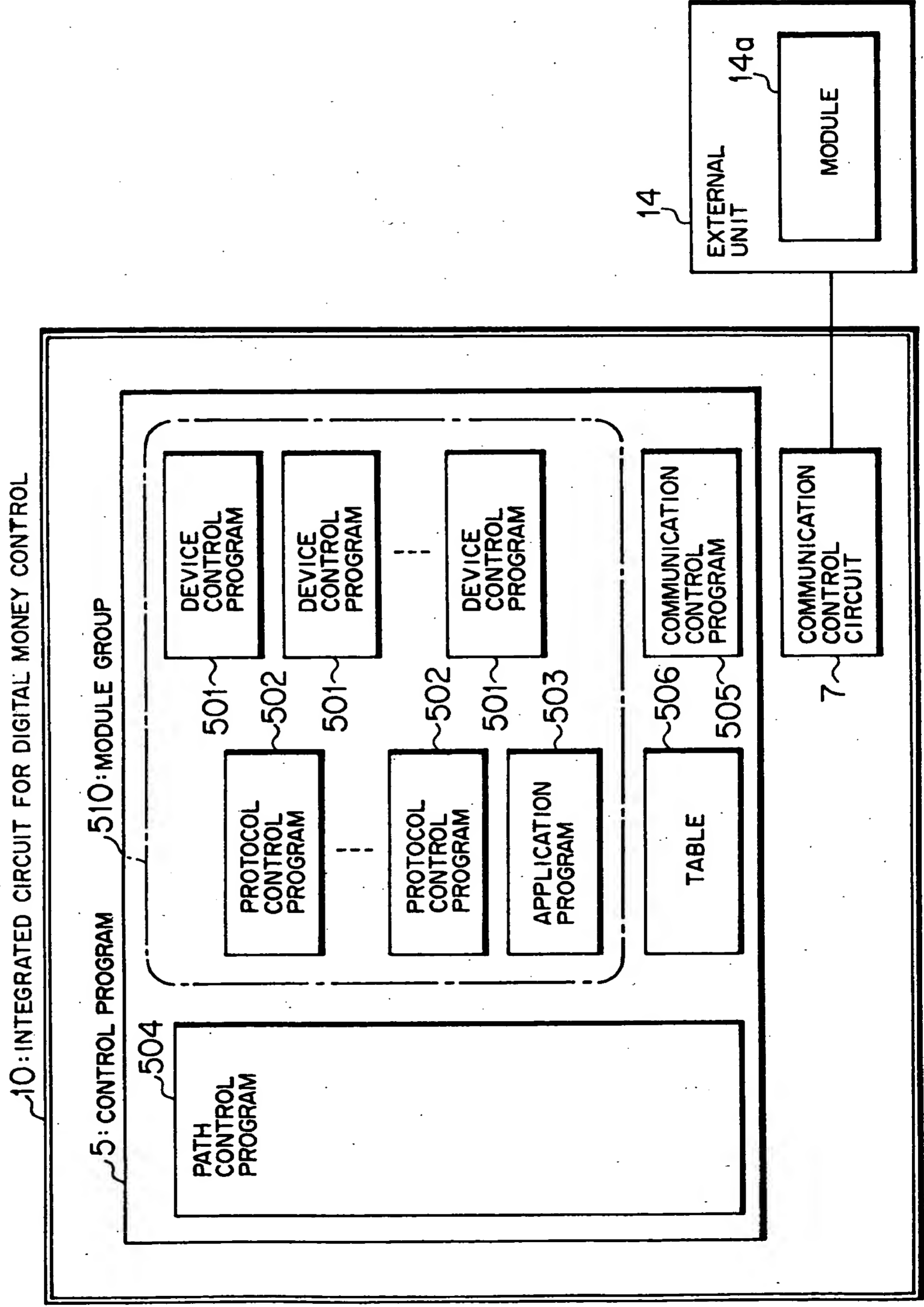


FIG. 9

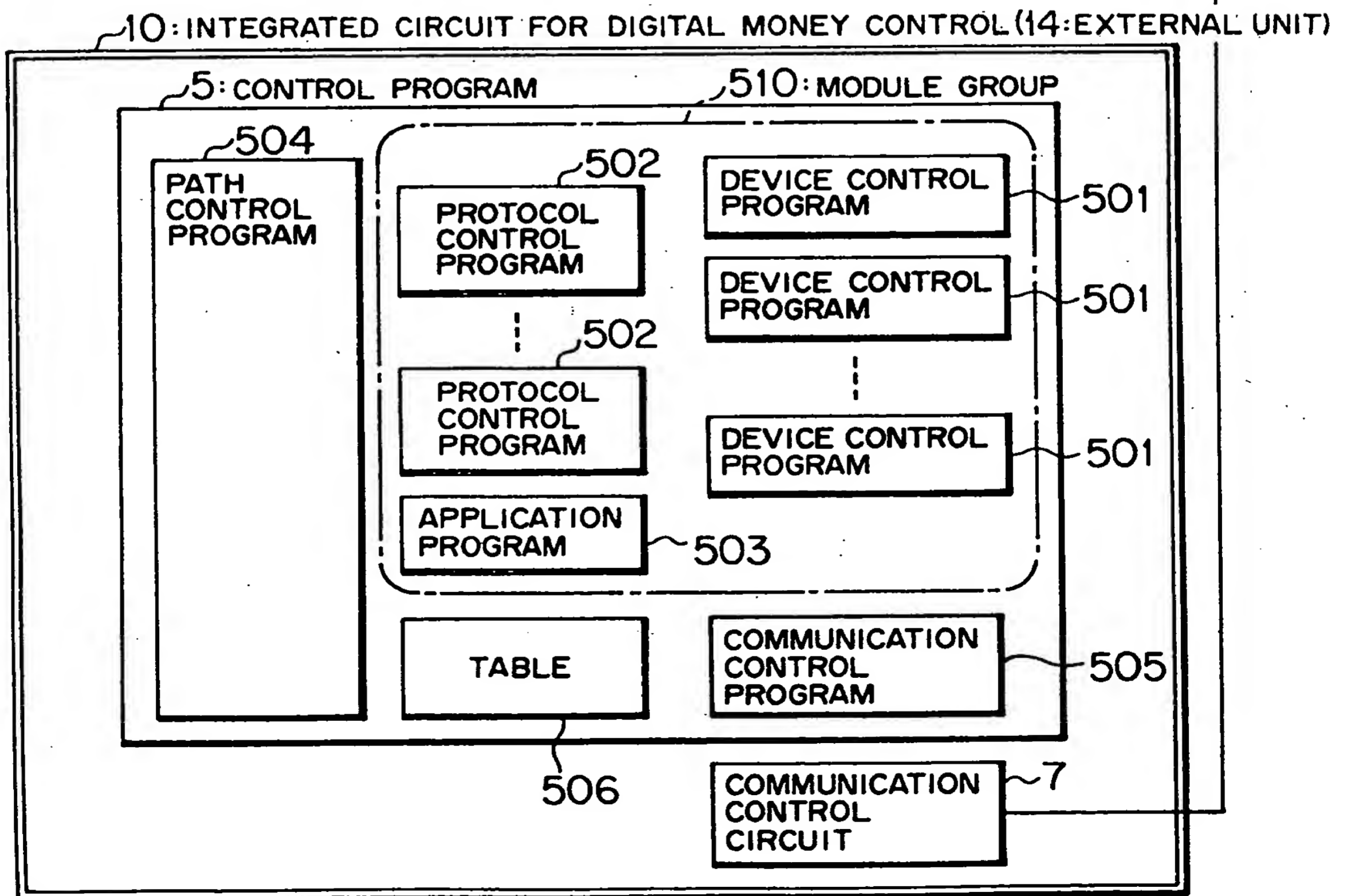
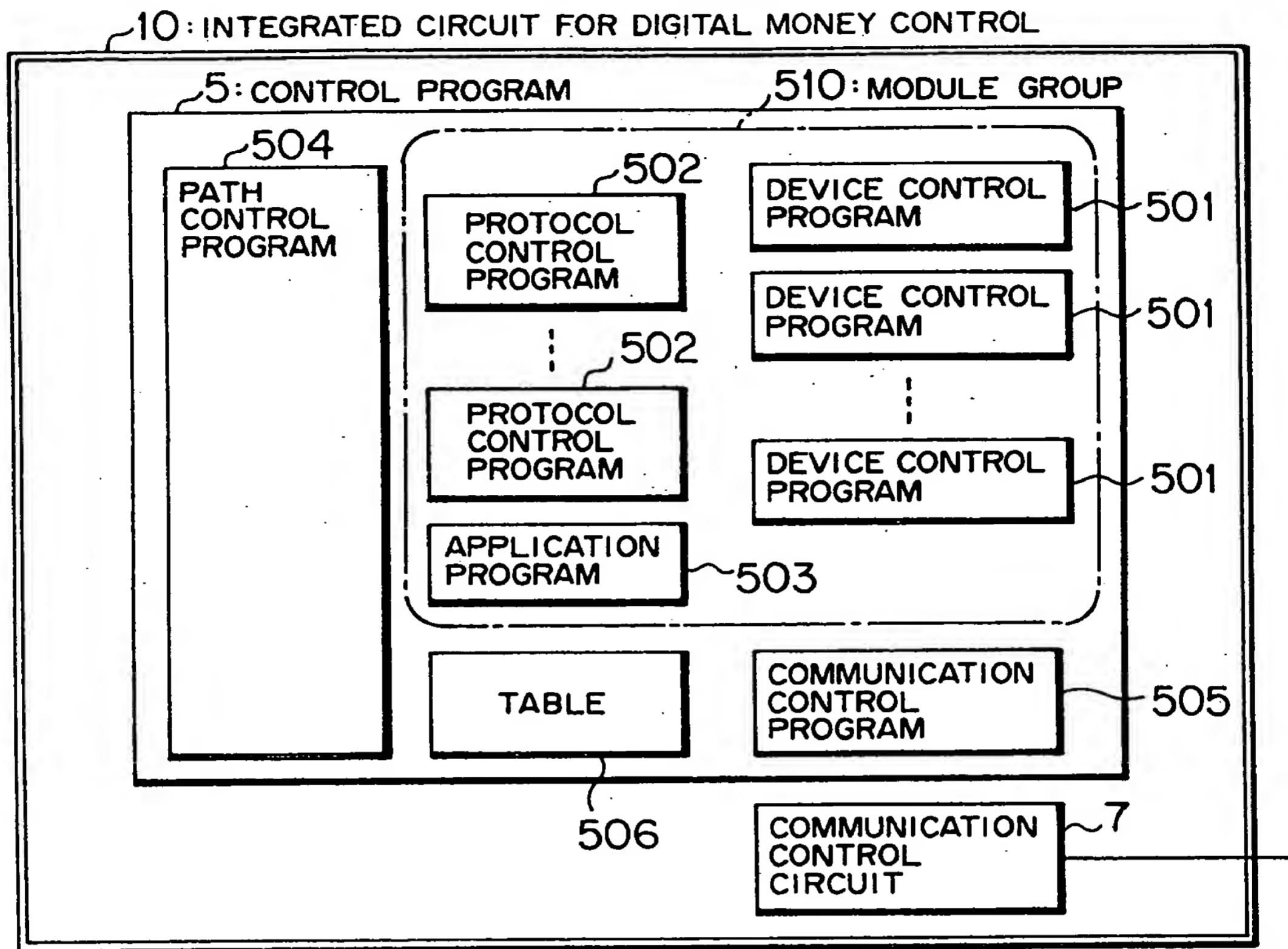


FIG. 10

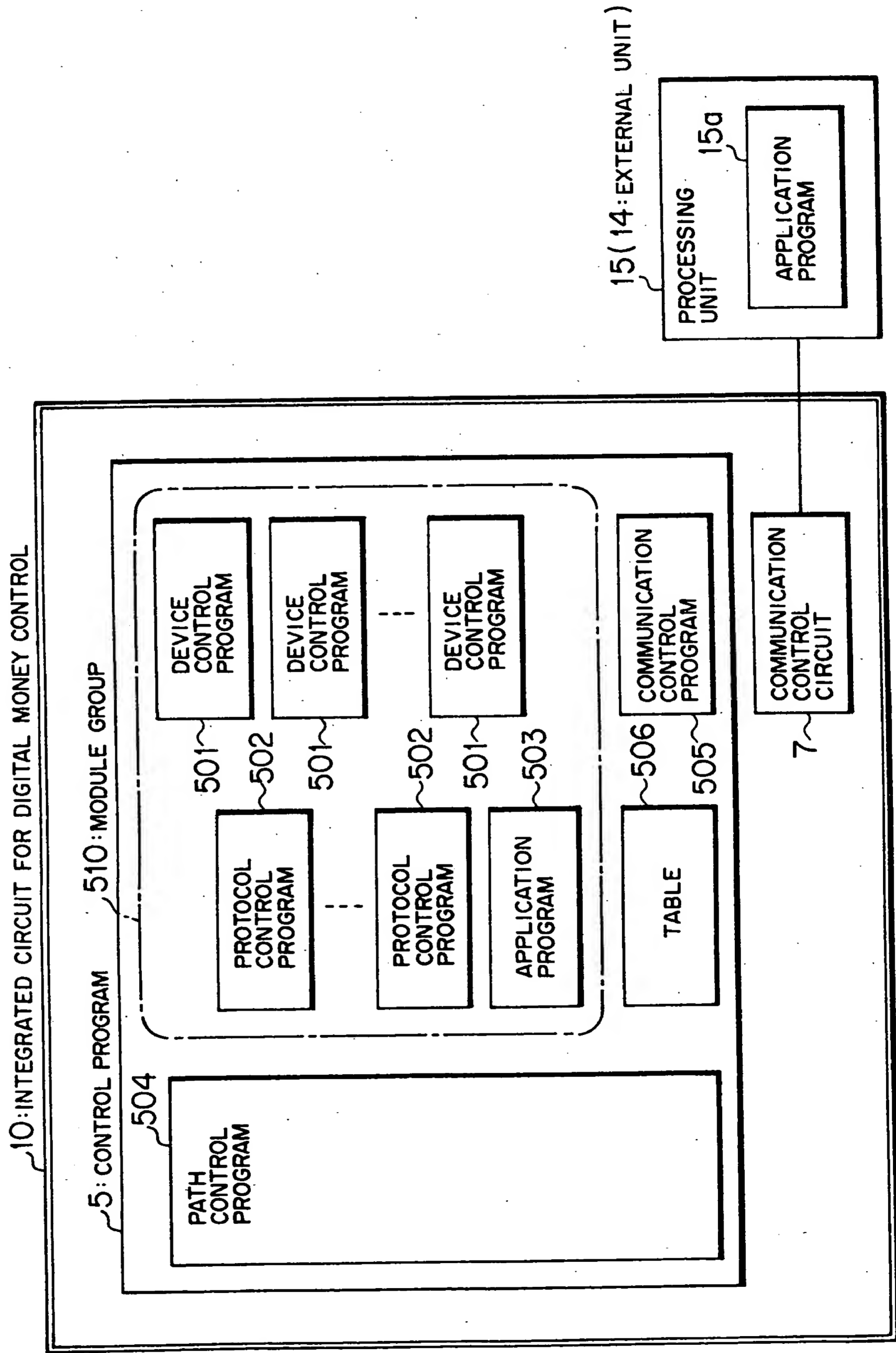


FIG. 11

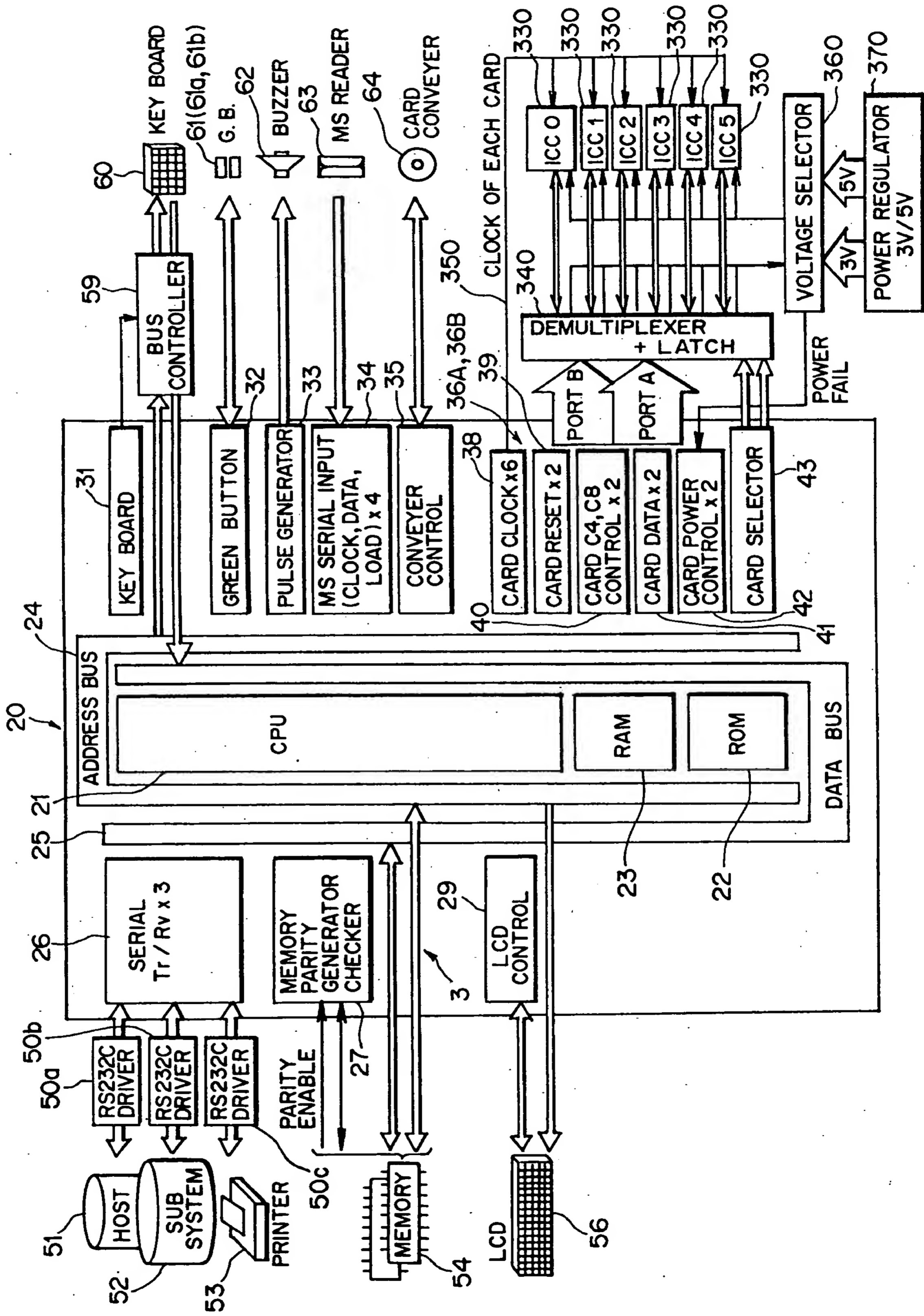


FIG. 12

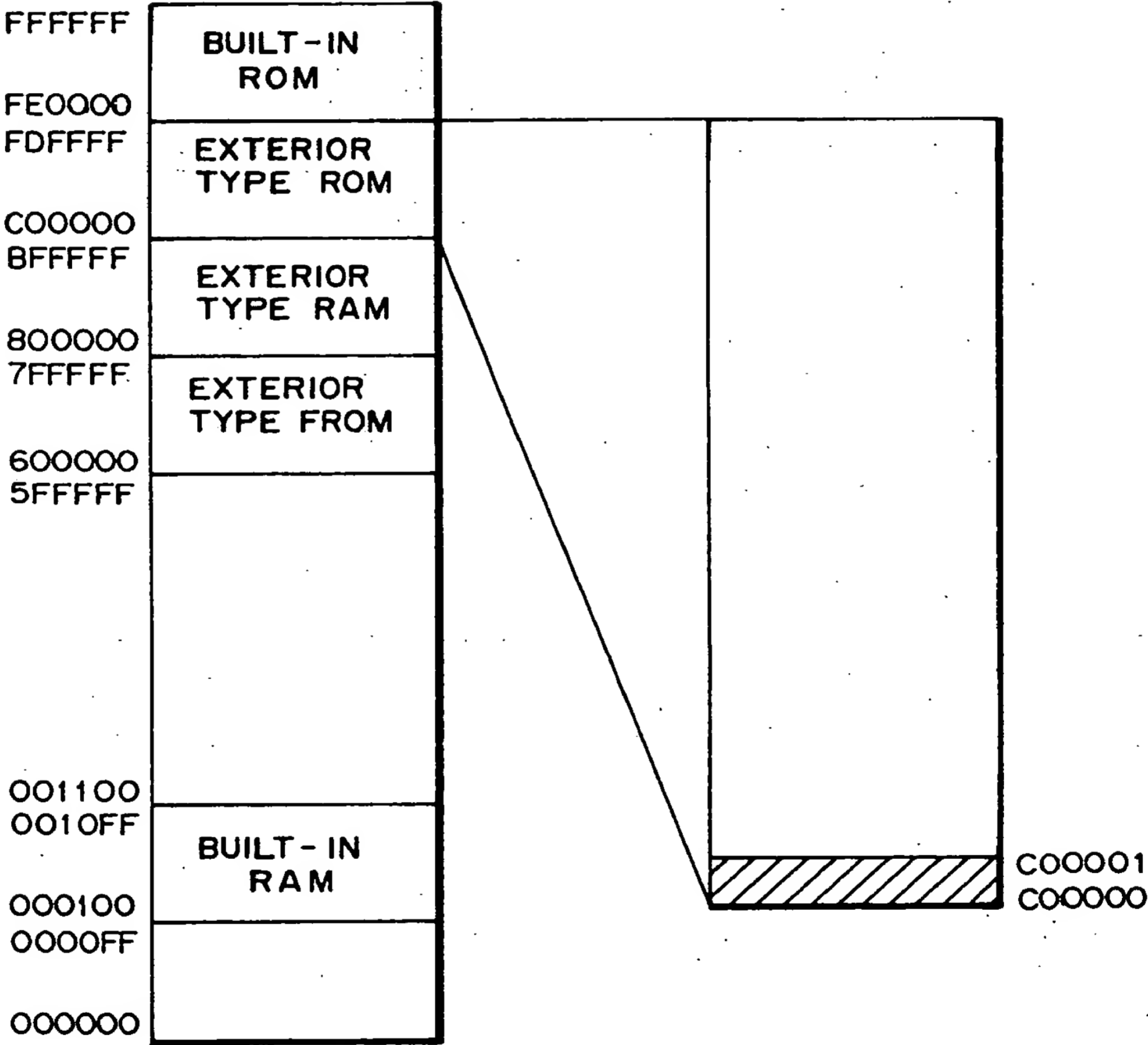


FIG. 13

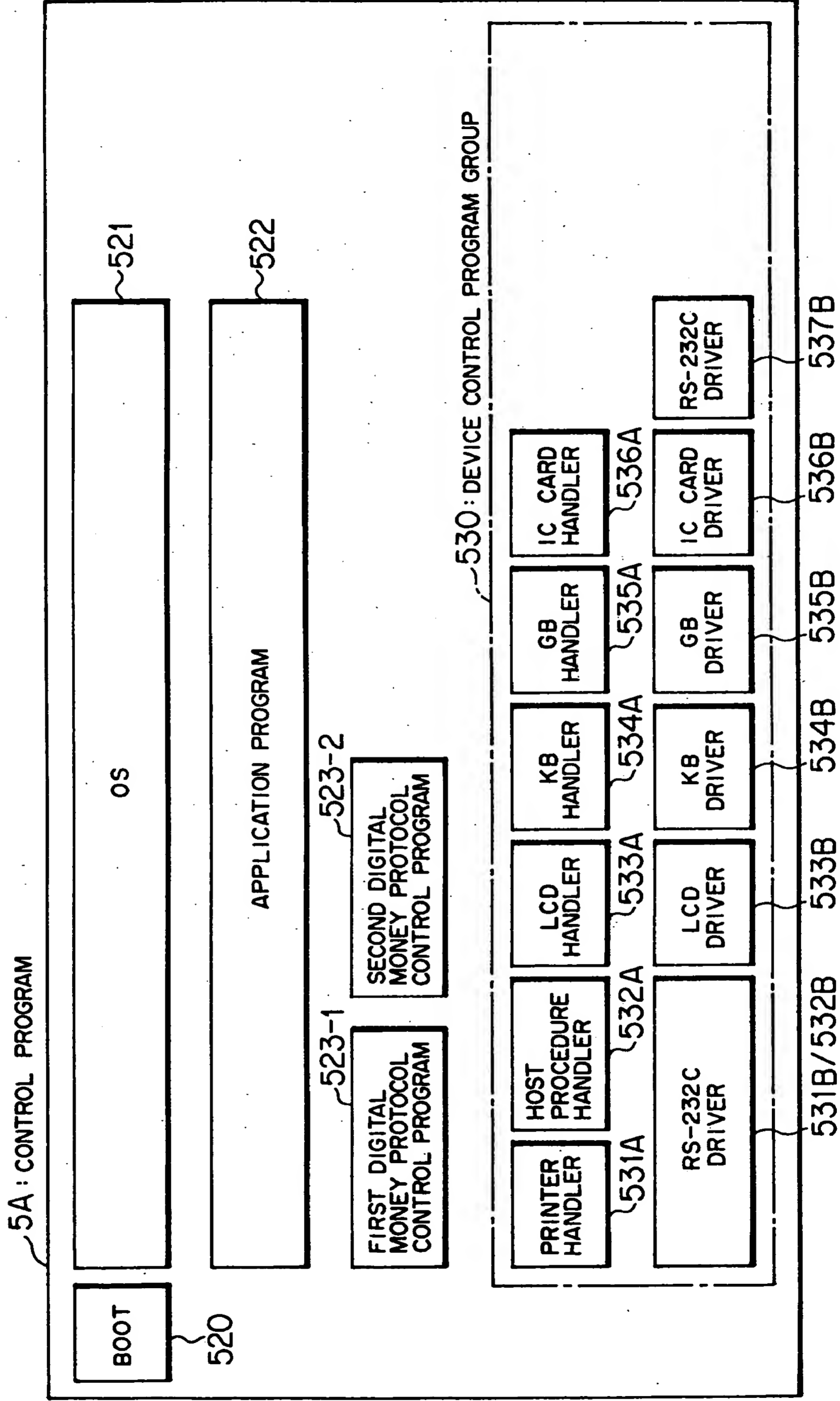


FIG. 14

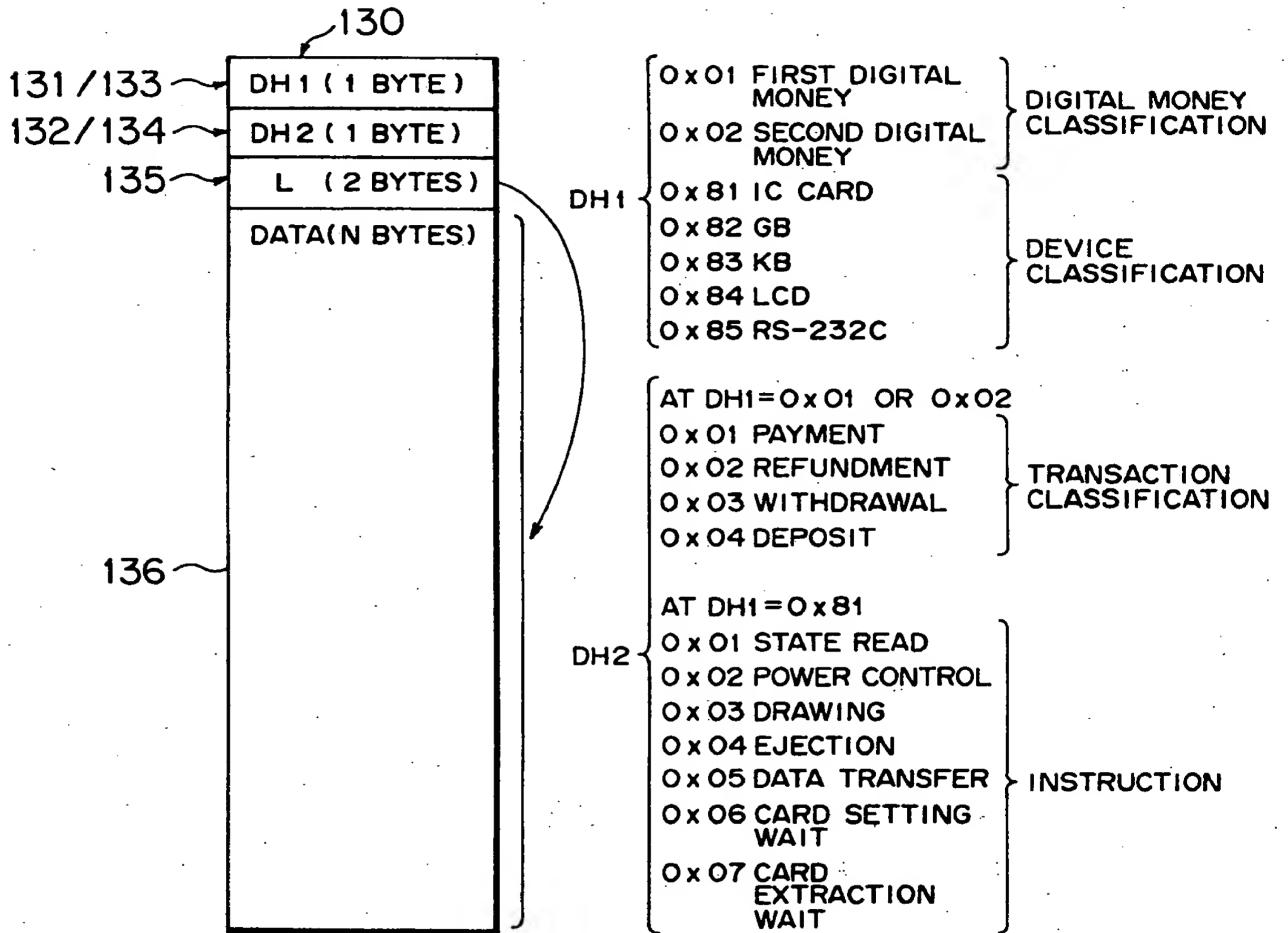


FIG. 15

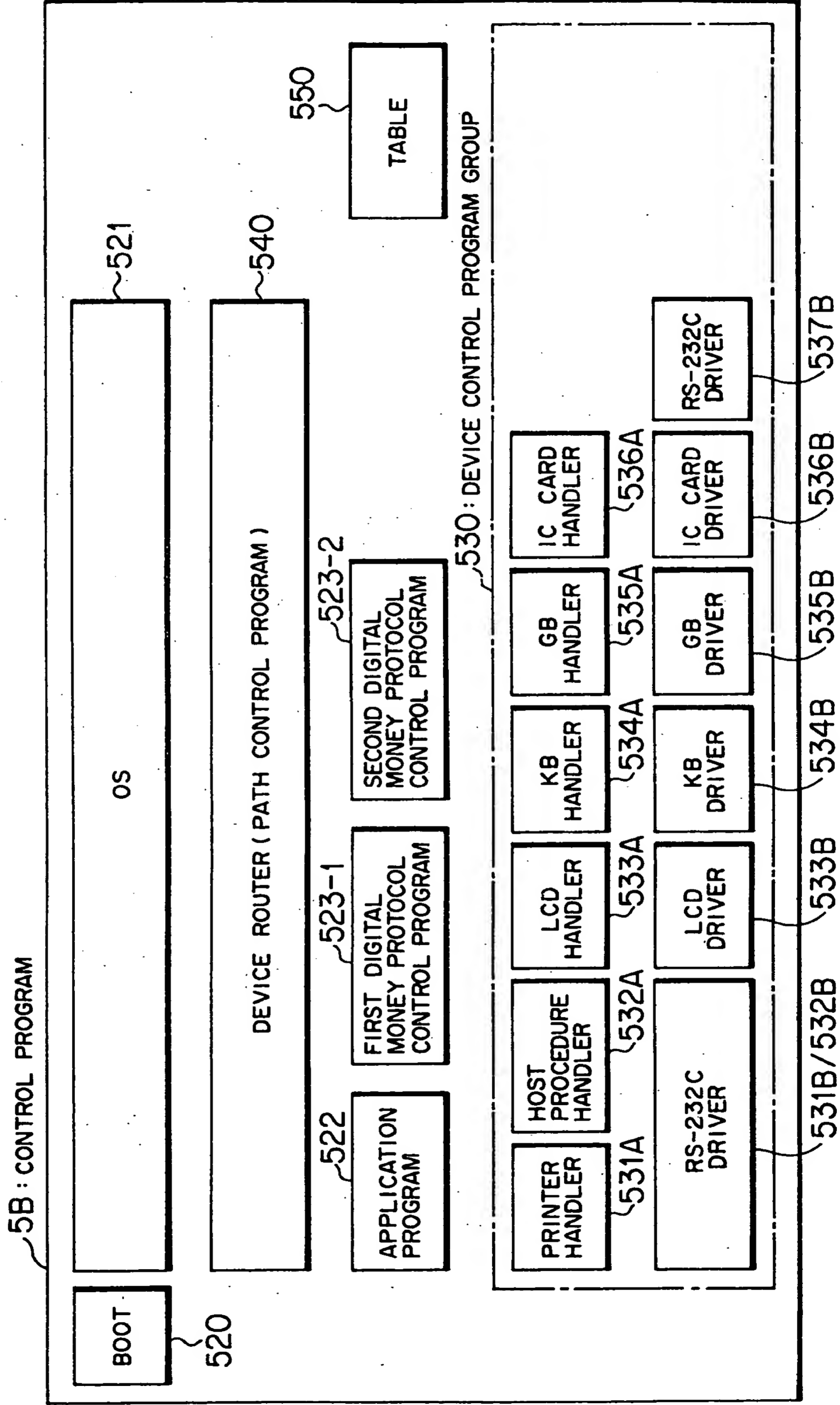


FIG. 16A

```

#90  ROOT
#01  IFD = #90  PORT = #1
#02  IFD = #01  PORT = #1
#03  IFD = #01  PORT = #2
#04  IFD = #02  PORT = #1
#05  IFD = #02  PORT = #2
    
```

550

FIG. 16B

[DEVICE DEFINITION]

```

#01  IFD = #01
#02  IFD = #02  ICCRW01
#03  IFD = #02  ICCRW02
#20  IFD = #01  LCD
#21  IFD = #01  KEY
    
```

550

FIG. 17

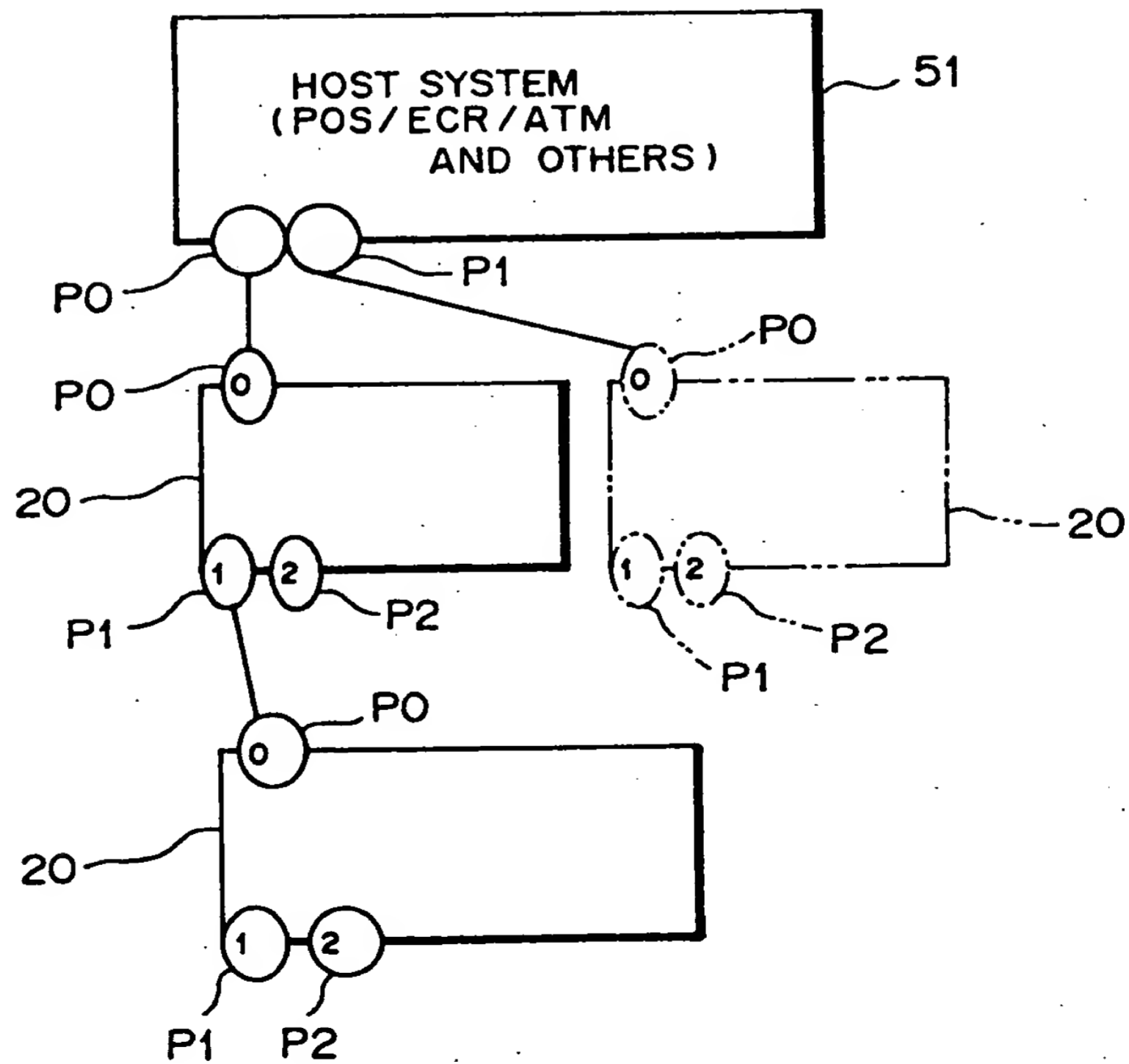


FIG. 20

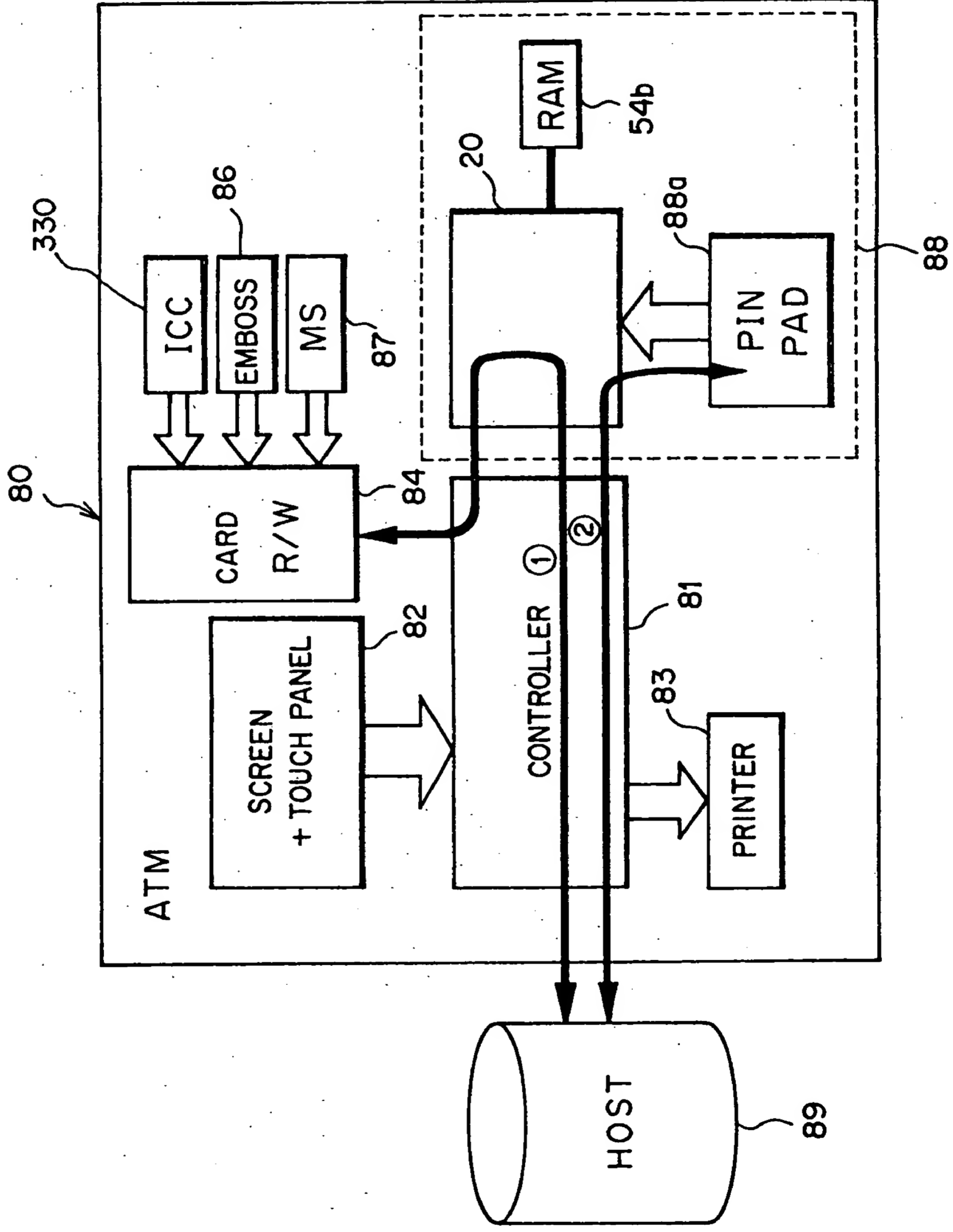


FIG. 21

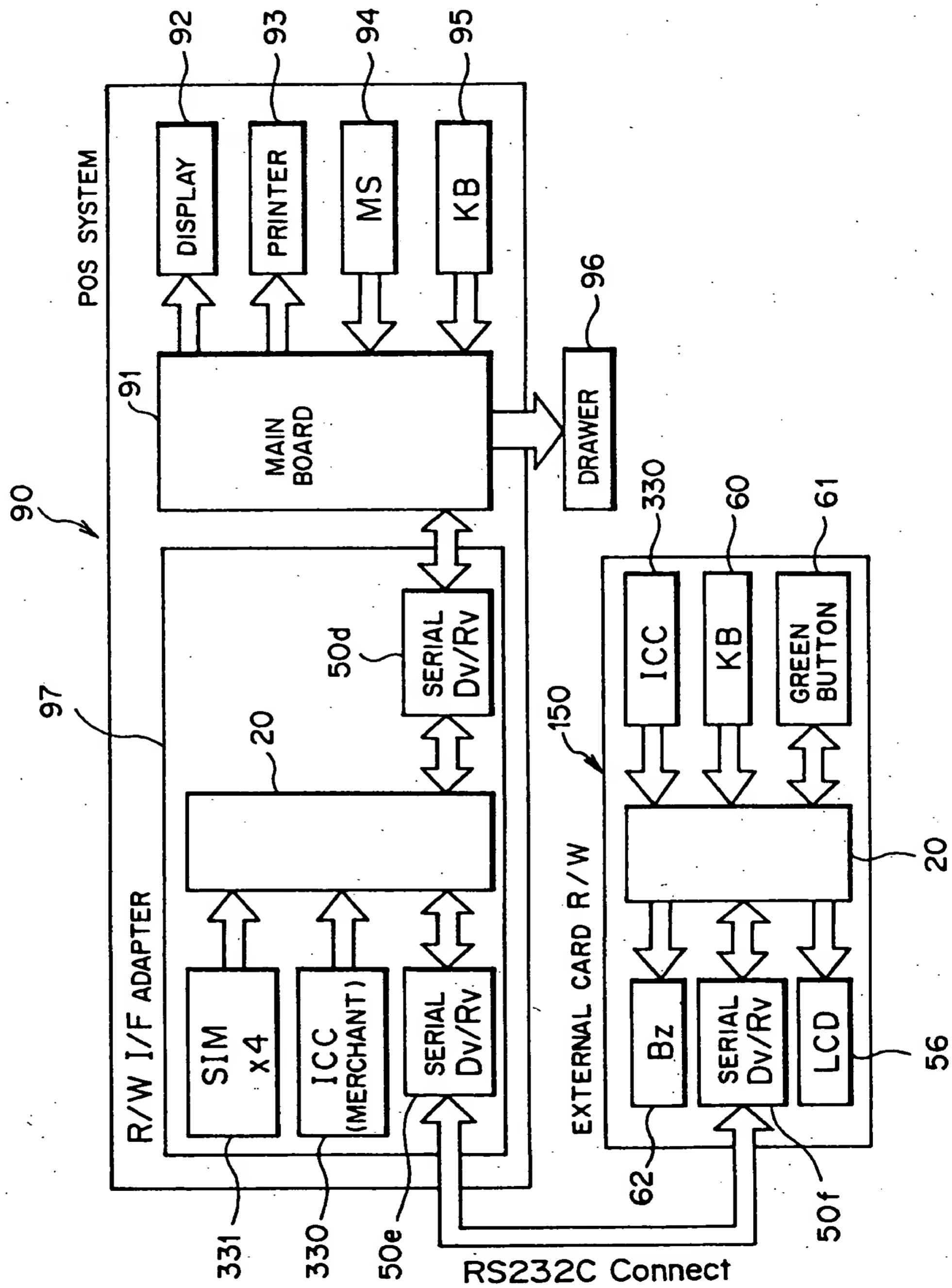


FIG. 22

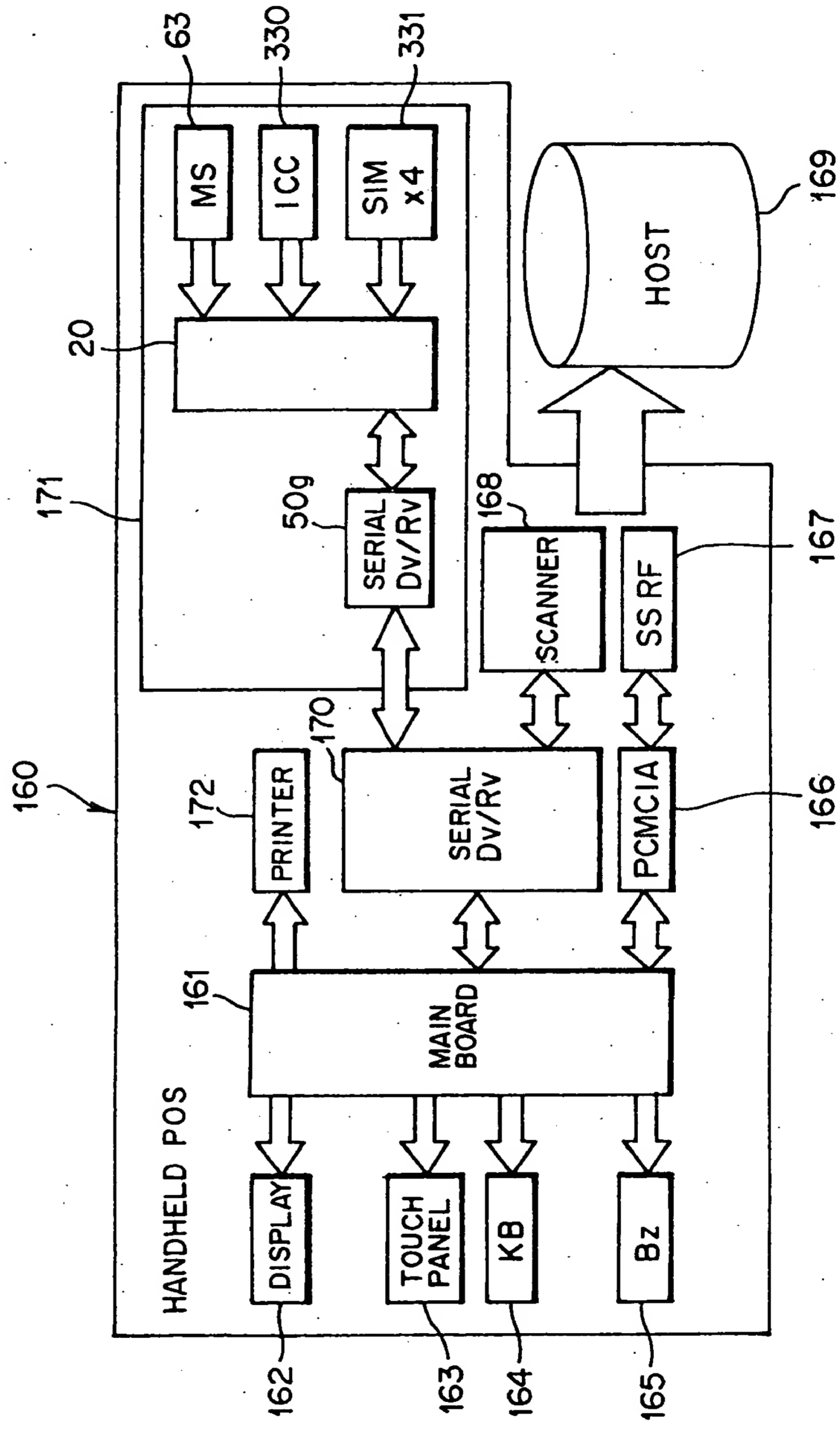


FIG. 23

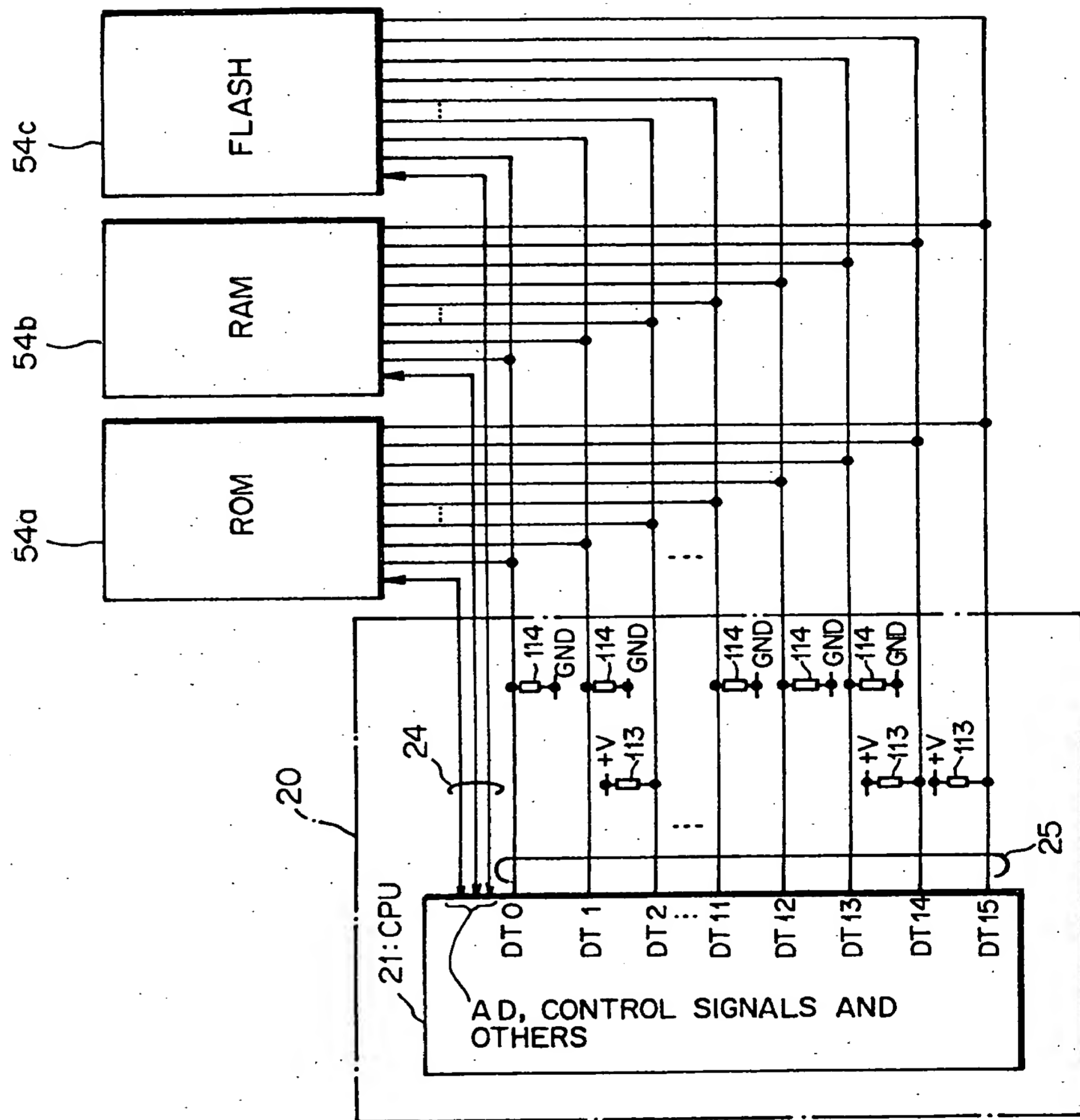


FIG. 24

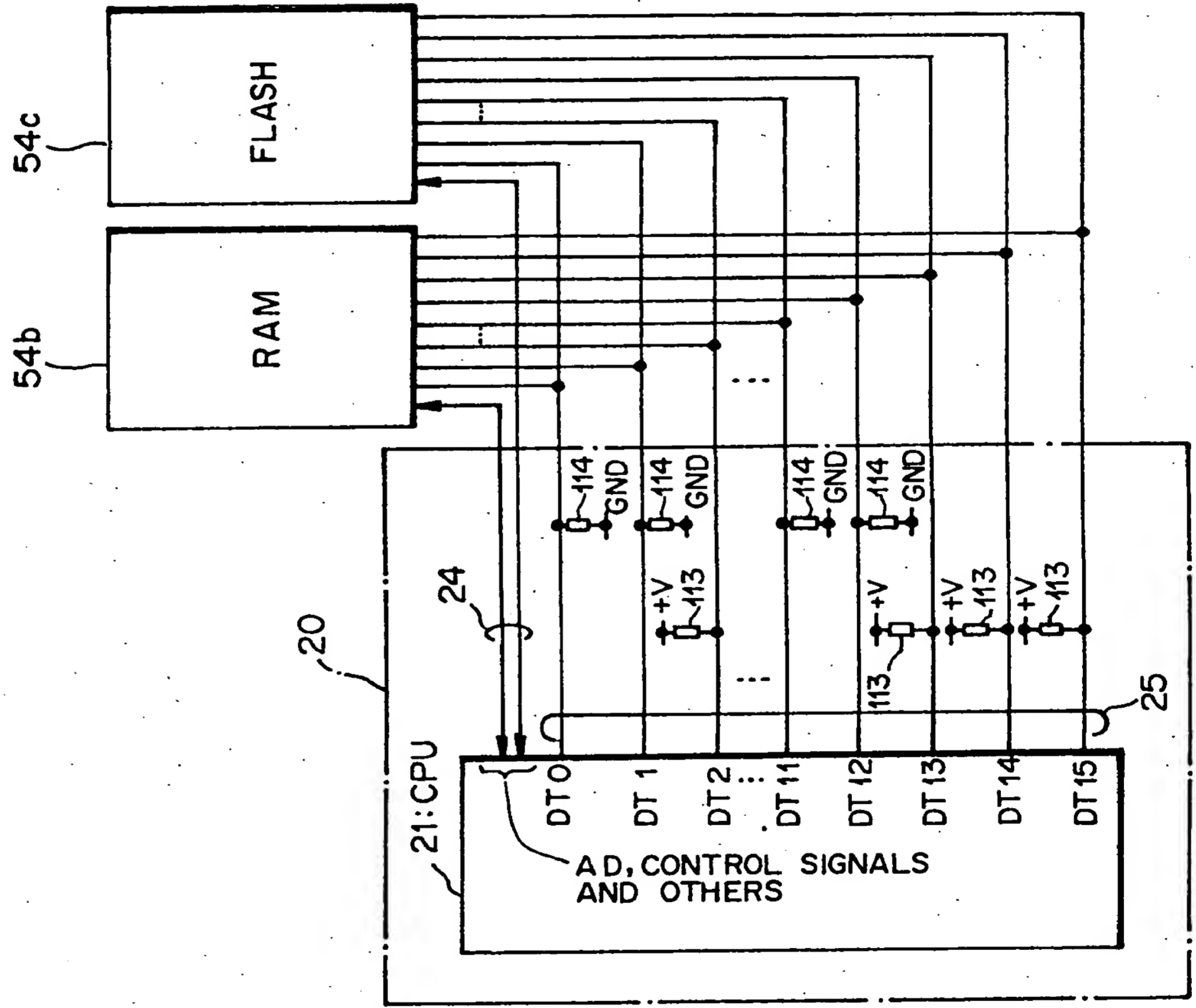


FIG. 25

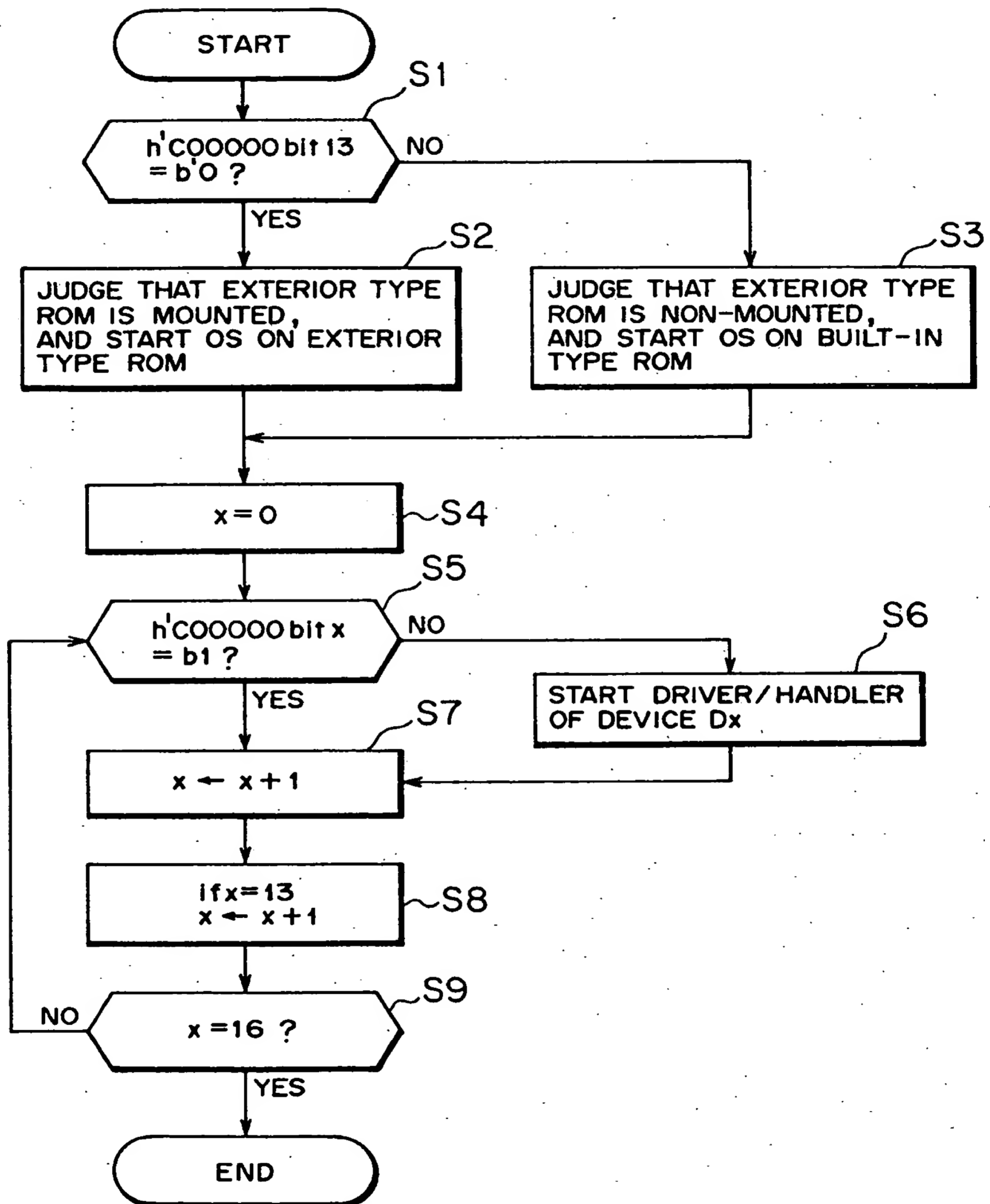


FIG. 26

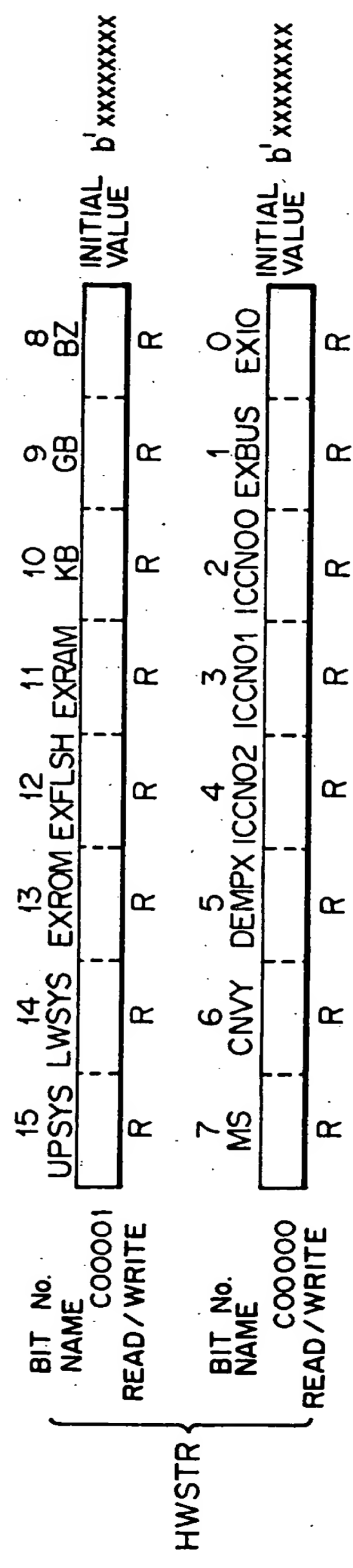


FIG.27

Bit No.	NAME	UNIT NAME	VALUE OF Bit	
			0	1
15	UPSYS	UPPER UNIT	CONNECTION	NON-CONNECTION
14	LWSYS	LOWER UNIT	CONNECTION	NON-CONNECTION
13	EXROM	EXTERIOR TYPE ROM	CONNECTION	NON-CONNECTION
12	EXFLSH	EXTERIOR TYPE FLASH	CONNECTION	NON-CONNECTION
11	EXRAM	EXTERIOR RAM	CONNECTION	NON-CONNECTION
10	KB	KEYBOARD	CONNECTION	NON-CONNECTION
9	GB	GREEN BUTTON	CONNECTION	NON-CONNECTION
8	BZ	BUZZER	CONNECTION	NON-CONNECTION
7	MS	MS READER	CONNECTION	NON-CONNECTION
6	CNVY	CONVEYER	CONNECTION	NON-CONNECTION
5	DEMPX	CARD SWITCH	CONNECTION	NON-CONNECTION
4	ICCNO2	NUMBER OF IC CARD TO BE CONNECTED	SEE FIG.28	
3	ICCNO1			
2	ICCNO0			
1	EXBUS	EXTENDED BUS	CONNECTION	NON-CONNECTION
0	EXIO	EXTENDED I/O	CONNECTION	NON-CONNECTION

FIG.28

DEMPX	ICCNO 2	ICCNO 1	ICCNO 0	NUMBER OF IC CARD TO BE CONNECTED
X	0	0	0	0
X	0	0	1	1
X	0	1	0	2
0	0	1	1	3
0	1	0	0	4
0	1	0	1	5
0	1	1	0	6

FIG. 31

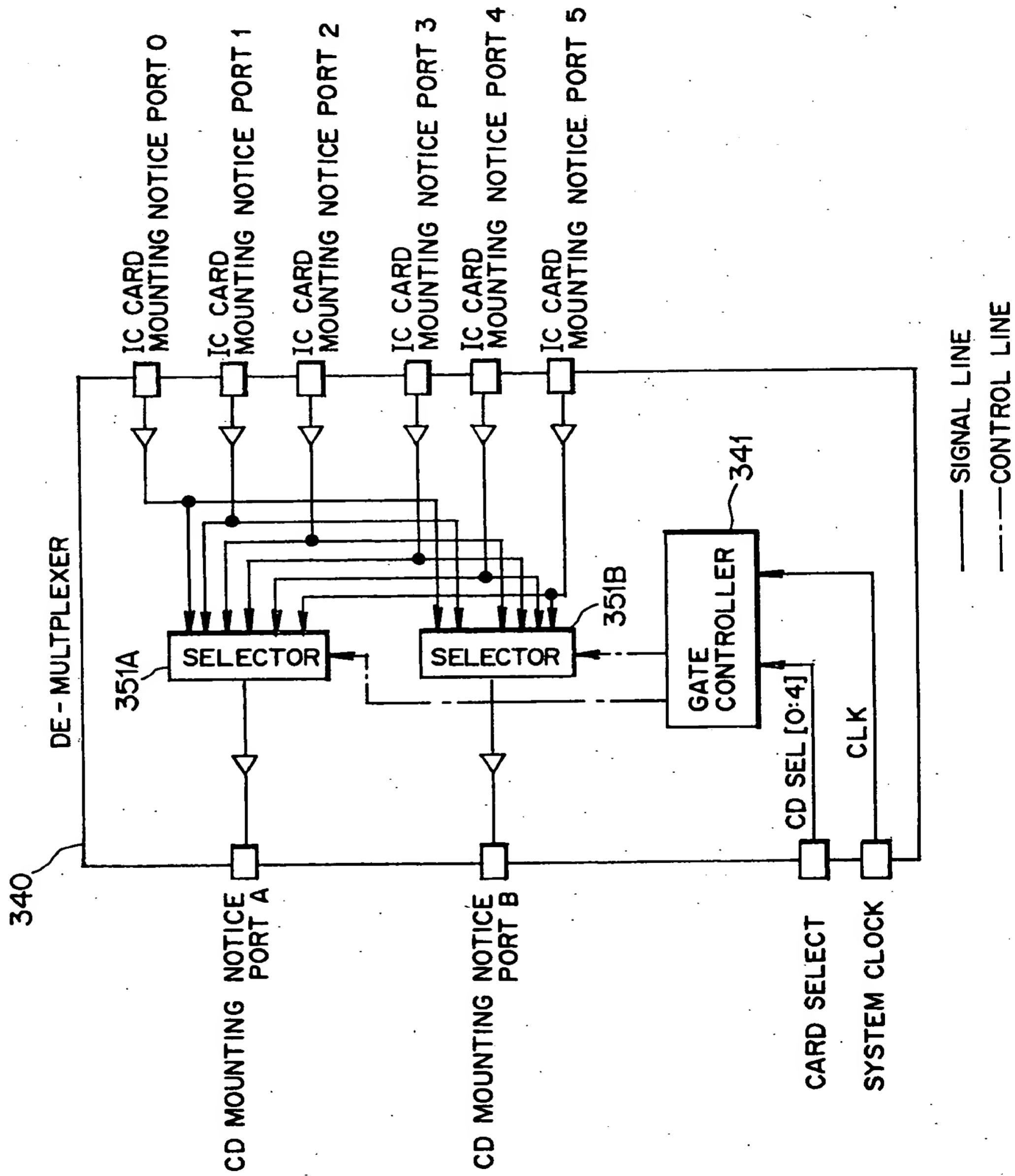


FIG. 32

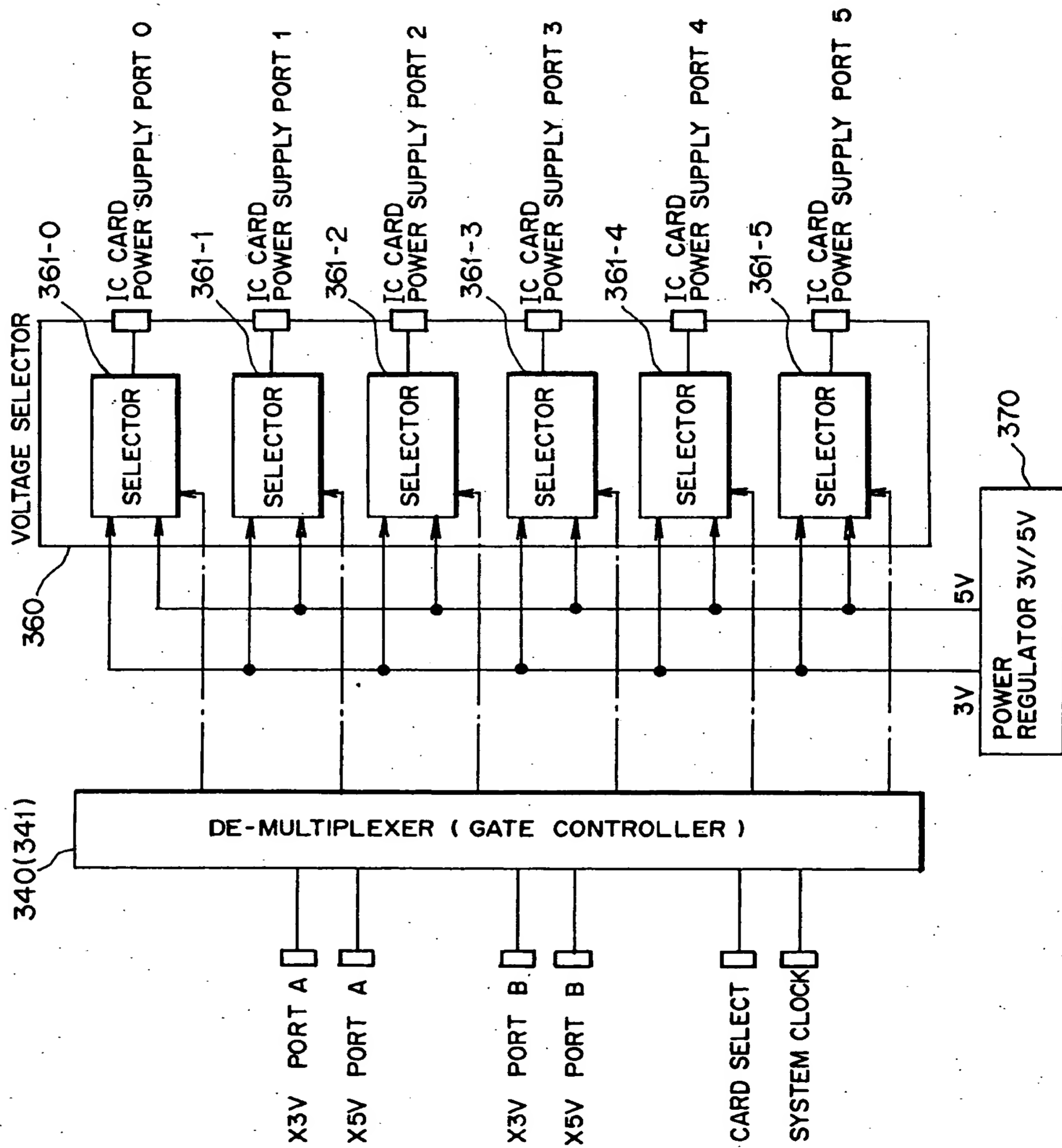


FIG. 33

Bit No. NAME	7	6	5	4	3	2	1	0	INITIAL VALUE
CDSEL	—	—	—	CDSEL 4	CDSEL 3	CDSEL 2	CDSEL 1	CDSEL 0	b'xxx00000
READ / WRITE	—	—	—	R/W	R/W	R/W	R/W	R/W	

FIG. 34

CDSEL 4	CDSEL 3	CDSEL 2	CDSEL 1	CDSEL 0	PORT A	PORT B
0	0	0	0	0	DEMULTIPLEXER RESET	
0	0	0	0	1	CARD 0 SELECTION	CARD 1 SELECTION
0	0	0	1	0	CARD 0 SELECTION	CARD 2 SELECTION
0	0	0	1	1	CARD 0 SELECTION	CARD 3 SELECTION
0	0	1	0	0	CARD 0 SELECTION	CARD 4 SELECTION
0	0	1	0	1	CARD 0 SELECTION	CARD 5 SELECTION
0	0	1	1	0	CARD 1 SELECTION	CARD 0 SELECTION
0	0	1	1	1	CARD 1 SELECTION	CARD 2 SELECTION
0	1	0	0	0	CARD 1 SELECTION	CARD 3 SELECTION
0	1	0	0	1	CARD 1 SELECTION	CARD 4 SELECTION
0	1	0	1	0	CARD 1 SELECTION	CARD 5 SELECTION
0	1	0	1	1	CARD 2 SELECTION	CARD 0 SELECTION
0	1	1	0	0	CARD 2 SELECTION	CARD 1 SELECTION
0	1	1	0	1	CARD 2 SELECTION	CARD 3 SELECTION
0	1	1	1	0	CARD 2 SELECTION	CARD 4 SELECTION
0	1	1	1	1	CARD 2 SELECTION	CARD 5 SELECTION
1	0	0	0	0	CARD 3 SELECTION	CARD 0 SELECTION
1	0	0	0	1	CARD 3 SELECTION	CARD 1 SELECTION
1	0	0	1	0	CARD 3 SELECTION	CARD 2 SELECTION
1	0	0	1	1	CARD 3 SELECTION	CARD 4 SELECTION
1	0	1	0	0	CARD 3 SELECTION	CARD 5 SELECTION
1	0	1	0	1	CARD 4 SELECTION	CARD 0 SELECTION
1	0	1	1	0	CARD 4 SELECTION	CARD 1 SELECTION
1	0	1	1	1	CARD 4 SELECTION	CARD 2 SELECTION
1	1	0	0	0	CARD 4 SELECTION	CARD 3 SELECTION
1	1	0	0	1	CARD 4 SELECTION	CARD 5 SELECTION
1	1	0	1	0	CARD 5 SELECTION	CARD 0 SELECTION
1	1	0	1	1	CARD 5 SELECTION	CARD 1 SELECTION
1	1	1	0	0	CARD 5 SELECTION	CARD 2 SELECTION
1	1	1	0	1	CARD 5 SELECTION	CARD 3 SELECTION
1	1	1	1	0	CARD 5 SELECTION	CARD 4 SELECTION
1	1	1	1	1	LATCH ALL OUTPUTS	

FIG. 35

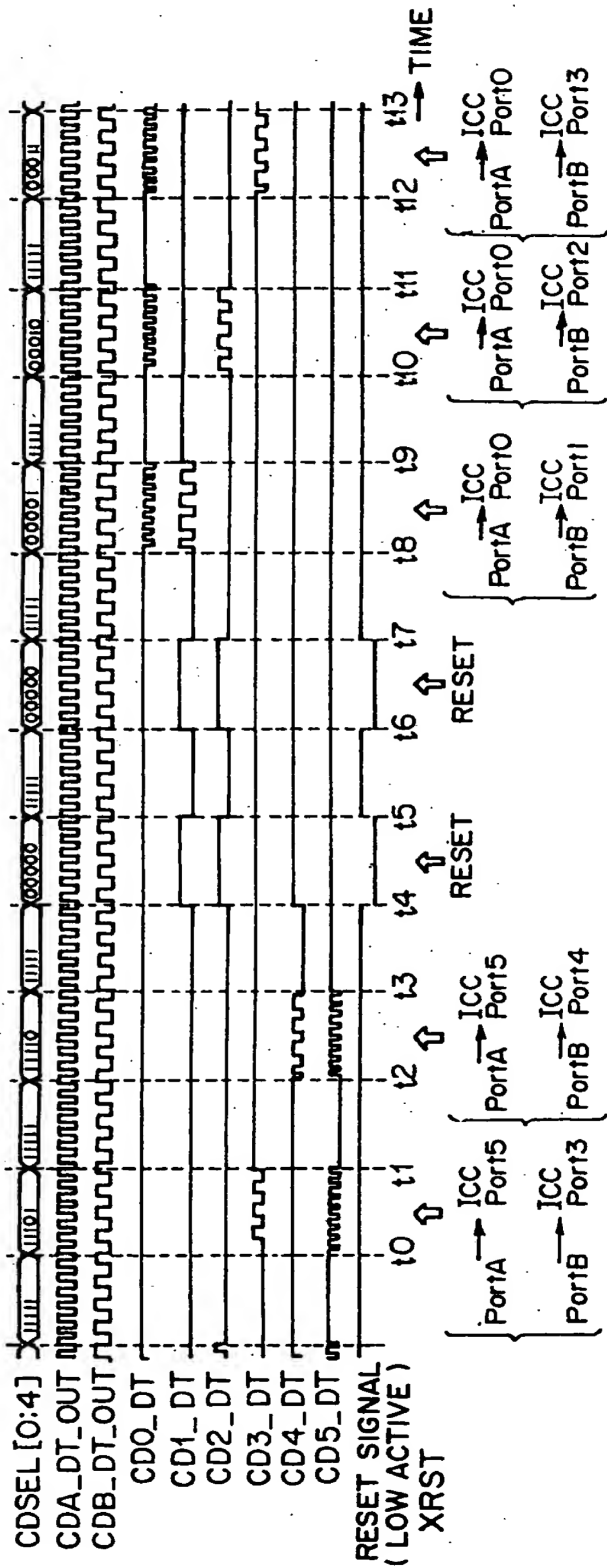


FIG. 36

